

VLSI IEEE PAPERS-2018

S.NO	TITLES	YEAR	CATEGORY
1	Systematic Design of an Approximate Adder: The Optimized Lower Part Constant-OR Adder	2018	LOW POWER
2	Low-Power Approximate Multipliers Using Encoded Partial Products and Approximate Compressors	2018	LOW POWER
3	A Simple Yet Efficient Accuracy-Configurable Adder Design	2018	LOW POWER
4	Improving Error Correction Codes for Multiple-Cell Upsets in Space Applications	2018	COMMUNICATION
5	Approximate Hybrid High Radix Encoding for Energy-Efficient Inexact Multipliers	2018	LOW POWER
6	A Method to Detect Bit Flips in a Soft-Error Resilient TCAM	2018	NETWORKING
7	An Energy-Efficient Network-on-Chip-Based Reconfigurable Viterbi Decoder Architecture	2018	COMMUNICATION
8	A Computationally Efficient Reconfigurable Constant Multiplication Architecture Based on CSD Decoded Vertical–Horizontal Common Sub-Expression Elimination Algorithm	2018	SIGNAL PROCESSING
9	LFSR-Based Test Generation for Path Delay Faults	2018	TESTING
10	On-Chip Self-Test Methodology with All Deterministic Compressed Test Patterns Recorded in Scan Chains	2018	TESTING
11	Secure Double Rate Registers as an RTL Countermeasure Against Power Analysis Attacks	2018	CRYPTOGRAPHY
12	An Energy-Efficient ECG Processor with Weak-Strong Hybrid Classifier for Arrhythmia Detection	2018	BIO-MEDICAL
13	Securing Emerging Nonvolatile Main Memory With Fast and Energy-Efficient AES In-Memory	2018	CRYPTOGRAPHY

14	Scalable Construction of Approximate Multipliers With Formally Guaranteed Worst Case Error	2018	LOW POWER
15	An FPGA-Based Phase Measurement System	2018	COMMUNICATION
16	Hardware Trojan detection by timing measurement: Theory and implementation	2018	TESTING
17	A FPGA based implementation of Sobel edge detection	2018	IMAGE PROCESSING
18	Approximate Reverse Carry Propagate Adder for Energy-Efficient DSP Applications	2018	LOW POWER
19	Leakage Power Attack-Resilient Symmetrical 8T SRAM Cell	2018	LOW POWER
20	Fast Binary Counters Based on Symmetric Stacking	2017	LOW POWER